

AMENDMENTS TO THE CLAIMS

1. (Canceled)

2. (Canceled)

3. (Previously Presented) An MPEG data recorder comprising:

an interface that receives data packets from a digital transmission line transmitting MPEG data in real time, and extracts predetermined MPEG data from received packets and outputs the extracted MPEG data as a data signal;

a data rate detector that determines a data rate of the MPEG data based on a valid data period, during which the data signal is outputted from the interface; and

a recording mode selector that selects a recording mode based on the determined data rate, wherein, the interface outputs a data signal as an MPEG packet having a predetermined amount of data, and outputs a synchronizing signal in synchronization with the MPEG packet; and

the data rate detector counts the synchronizing signals outputted in a predetermined duration for detection, and detects the data rate of MPEG data based on the counted value.

4. (Currently Amended) An MPEG data recorder comprising:

an interface that receives data packets from a digital transmission line transmitting MPEG data in real time, and extracts predetermined MPEG data relating to

the same content from received packets and outputs the extracted MPEG data as a data signal;

a data rate detector that determines a data rate of the MPEG data based on a valid data period, during which the data signal is outputted from the interface;

a recording mode selector that selects a recording mode based on the determined data rate; and

a motor driver that controls the speed of a recording media based on the selected recording mode. The MPEG data recorder as claimed in claim 1, wherein, the data rate detector adds up valid data periods in a predetermined period for detection including two or more valid data periods, and detects the data rate of MPEG data based on the added-up value.

5. (Currently Amended) An MPEG data recorder comprising:

an interface that receives data packets from a digital transmission line transmitting MPEG data in real time, and extracts predetermined MPEG data relating to the same content from received packets and outputs the extracted MPEG data as a data signal;

a data rate detector that determines a data rate of the MPEG data based on a valid data period, during which the data signal is outputted from the interface;

a recording mode selector that selects a recording mode based on the determined data rate; and

a motor driver that controls the speed of a recording media based on the selected recording mode wherein,

the interface outputs a transmission control signal when the interface outputs a data signal,

the data rate detector determines a percentage occupied by the valid data period, by detecting the transmission control signal; ~~(Previously Amended) The MPEG data recorder as claimed in claim 2, wherein, and~~

the data rate detector adds up valid data periods in a predetermined period for detection including two or more valid data periods, and detects the data rate of MPEG data based on the added-up value.

6. (Previously Presented) The MPEG data recorder as claimed in claim 3, wherein, the data rate detector adds up valid data periods in a predetermined period for detection including two or more valid data periods, and detects the data rate of MPEG data based on the added-up value.

7. (Canceled)

8. (Canceled)

9. (Previously Presented) The MPEG data recorder as claimed in claim 3, the interface is an IEEE 1394 interface unit, which performs an isochronous communication

through an IEEE 1394 link, and extracts MPEG data of a predetermined channel from the received packet.

10. (Previously Presented) An MPEG data recorder comprising:
an interface that receives data packets from a digital transmission line
transmitting MPEG data in real time, and extracts predetermined MPEG data from
received packets and outputs the extracted MPEG data as a data signal;
a data rate detector that determines a data rate of the MPEG data based on a
valid data period, during which the data signal is outputted from the interface; and
a recording mode selector that selects a recording mode based on the
determined data rate, wherein, the interface outputs a data signal as an MPEG packet
having a predetermined amount of data, and outputs a synchronizing signal in
synchronization with the MPEG packet; and
the data rate detector counts the synchronizing signals outputted in a
predetermined duration for detection, and detects the data rate of MPEG data based on
the counted value.;
wherein the interface is an IEEE 1394 interface unit, which performs an
isochronous communication through an IEEE 1394 link, and extracts MPEG data of a
predetermined channel from the received packet, and~~The MPEG data recorder as~~
~~claimed in claim 7,~~

wherein, the data rate detector adds up valid data periods in a predetermined period for detection including two or more isochronous cycles, and detects the data rate of MPEG data based on the added-up value.

11. ((Currently Amended) An MPEG data recorder comprising:

an interface that receives data packets from a digital transmission line transmitting MPEG data in real time, and extracts predetermined MPEG data relating to the same content from received packets and outputs the extracted MPEG data as a data signal;

a data rate detector that determines a data rate of the MPEG data based on a valid data period, during which the data signal is outputted from the interface;

a recording mode selector that selects a recording mode based on the determined data rate;

a motor driver that controls the speed of a recording media based on the selected recording mode; wherein,

the interface outputs a transmission control signal when the interface outputs a data signal.

the data rate detector determines a percentage occupied by the valid data period, by detecting the transmission control signal, and

~~The MPEG data recorder as claimed in claim 2;~~ the interface is an IEEE 1394 interface unit, which performs an isochronous communication through an

IEEE 1394 link, and extracts MPEG data of a predetermined channel from the received packet.

12. (Previously Presented) The MPEG data recorder as claimed in claim 9, wherein, the data rate detector adds up valid data periods in a predetermined period for detection including two or more isochronous cycles, and detects the data rate of MPEG data based on the added-up value.

13. (Canceled)

14. (Canceled)